

Docket No.: 043876-0155



PATENT

*IFW*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 20277
Craig HANSEN, et al.	:	Confirmation Number: 4560
Application No.: 10/757,516	:	Group Art Unit: 2183
Filed: January 15, 2004	:	Examiner: COLEMAN, ERIC
For: PROGRAMMABLE PROCESSOR AND SYSTEM FOR STORE MULTIPLEX OPERATION		

Mail Stop Information Disclosure Statement  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is an Information Disclosure Statement and Form 1449 (w/references) in the above-identified application.



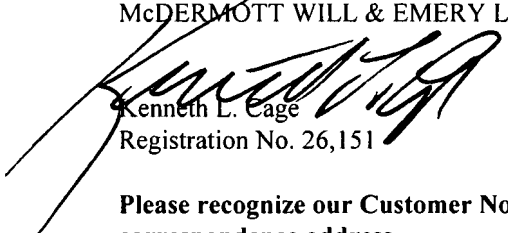
No additional fee is required.  
Applicant is entitled to small entity status under 37 CFR 1.27



The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

  
Kenneth L. Cage  
Registration No. 26,151

600 13<sup>th</sup> Street, N.W.  
Washington, DC 20005-3096  
Phone: 202.756.8000 KLC/jam  
Facsimile: 202.756.8087  
Date: July 10, 2007

Please recognize our Customer No. 20277 as our  
correspondence address.

Docket No.: 043876-0155



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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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For: PROGRAMMABLE PROCESSOR AND SYSTEM FOR STORE MULTIPLEX OPERATION		

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Mail Stop Ex Partes Reexam  
Central Reexamination Unit  
Office of Patent Legal Administration  
United States Patent & Trademark Office  
P.O. Box 1450  
Alexandria, VA 22313-1450

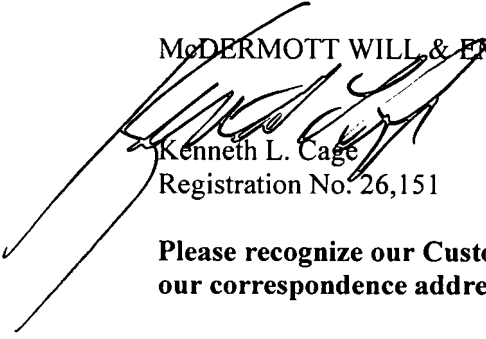
Dear Sir:

In accordance with 37 CFR §1.97, §1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached from PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom. Please note that also included on the PTO-1449 is a patent family tree for U.S. patent no. 5,742,840.

Please charge any shortage of fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please fully credit any excess fees to such deposit account.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

  
Kenneth L. Cage  
Registration No. 26,151

600 13<sup>th</sup> Street, N.W.  
Washington, DC 20005-3096  
Phone: 202.756.8000 KLC:jwp  
Facsimile: 202.756.8087  
**Date: July 10, 2007**

**Please recognize our Customer No. 20277 as  
our correspondence address.**



□ □

PTO/SB/08a 07-05  
Approved for use through 07/31/2006. OMB 0651-0031  
U. S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE  
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

<b>Substitute for form 1449A/PTO</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)			<b>Complete if Known</b>		
			Application Number	10/757,516	
			Filing Date	January 15, 2004	
			First Named Inventor	Craig Hansen	
			Group Art Unit	2183	
			Examiner Name	COLEMAN, ERIC	
Sheet	1	of	4	Attorney Docket Number	43876-155

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
	A	US-4,509,119	04-02-1985	Gumaer	
	B	US-4,943,919	07-24-1990	Aslin	
	C	US-5,208,914	05-04-1993	Wilson	
	D	US-5,253,342	10-12-1993	Blount	
	E	US-5,256,994	10-26-1993	Langendorf	
	F	US-5,287,327	02-15-1994	Takasugi	
	G	US-5,327,570	07-05-1994	Foster	
	H	US-5,371,772	12-06-1994	Al-Khairi	
	I	US-5,375,208	12-20-1994	Pitot	
	J	US-5,477,181	12-19-1995	Li	
	K	US-5,522,054	05-28-1996	Gunlock	
	L	US-5,530,960	06-25-1996	Parks	
	M	US-5,826,106	10-20-1998	Pang	
	N	US-5,828,869	10-27-1998	Johnson	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
	O	WO 97/07450	02-27-1997	Hansen		

Examiner Signature		Date Considered	
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\*EXAMINER: Initial reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard St.16 if possible. 6 Applicant is to place a check mark here if English language translation is attached. The collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P. O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORM TO THIS ADDRESS. Send To Commissioner For Patents, P. O. Box 1450, Alexandria, VA 22313-1450. If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2

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				Filing Date	January 15, 2004
				First Named Inventor	Craig Hansen
				Group Art Unit	2183
				Examiner Name	COLEMAN, ERIC
Sheet	2	of	4	Attorney Docket Number	43876-155

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate) title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issued number(s), publisher, city and/or country where published.	T <sup>2</sup>
	P	11/2/05 Complaint for Patent Infringement ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	Q	2/16/06 SCEA's Motion for Stay of Litigation Pending Reexamination ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	R	3/1/06 MicroUnity's Opposition to Defendant's Motion for Stay of Litigation Pending Reexamination ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	S	3/1/06 MicroUnity's Answer to SCEA's Counterclaim ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	T	3/6/06 SCEA's Reply Supporting Its Motion for Stay of Litigation Pending Reexamination ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	U	9/29/06 Order Denying Motion to Continue, Finding as Moot Motion to Stay ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	V	12/1/06 MicroUnity's Disclosure of Asserted Claims And Preliminary Infringement Contentions Pursuant to Local Patent Rule 3-1 and Attachments A-Z, AA, and BB ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	W	12/1/06 MicroUnity's Identification of Documents Pursuant to Local Patent Rule 3-2 ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	X	12/20/06 MicroUnity's Initial Disclosures ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	Y	12/21/06 SCEA's Initial Disclosures Pursuant to the Discovery Order ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	Z	1/25/07 First Amended Complaint for Patent Infringement and Exhibits A-J ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AA	2/12/07 SCEA's Answer, Affirmative Defenses, And Counterclaims to MicroUnity's First Amended Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	

Examiner Signature		Dated	
		Considered	

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(use as many sheets as necessary)</i>		<i>Complete if Known</i>			
		Application Number	10/757,516		
		Filing Date	January 15, 2004		
		First Named Inventor	Craig Hansen		
		Group Art Unit	2183		
		Examiner Name	COLEMAN, ERIC		
Sheet	3	of	4	Attorney Docket Number	43876-155

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate) title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issued number(s), publisher, city and/or country where published.	T <sup>2</sup>
	AB	3/2/07 MicroUnity's Answer to SCEA's Counterclaim in Response to MicroUnity's First Amended Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AC	3/5/07 SCEA's Invalidity Contentions & Attachments A-I ( <b>portions of G and H are Outside Counsel Eyes Only and are not included</b> ) ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AD	4/3/07 SCEA's Supplemental Disclosures ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AE	5/1/07 SCEA's Supplemental Invalidity Contentions & Attachments A-I ( <b>Exhibits A2, E2, F2, G9 and H7 are Outside Counsel Eyes Only and are not included</b> ) ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AF	5/9/07 SCEA's Amended Answer, Affirmative Defenses, And Counterclaims to MicroUnity's First Amended Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AG	5/22/07 MicroUnity's LPR 4-1 Proposed List of Terms to be Construed ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AH	5/22/07 SCEA's Proposed List of Claim Terms, Phrases, Clauses, And Elements For Construction ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AI	5/29/07 MicroUnity's Answer to SCEA's Amended Counterclaim in Response to MicroUnity's First Amended Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AJ	11/22/06 Complaint Against Advanced Micro Devices, Inc. and Exhibits A-L ( <i>MicroUnity Systems Engineering, Inc. v. Advanced Micro Devices, Inc.</i> ; 2:06-cv-486; USDC for the Eastern District of Texas, Marshall Division)	
	AK	2/26/07 AMD's Answer to MicroUnity's Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Advanced Micro Devices, Inc.</i> ; 2:06-cv-486; USDC for the Eastern District of Texas, Marshall Division)	
	AL	Advance Datasheet for MT48LC2M8S1(S) 2 Meg x 8 SDRAM, Micron Semiconductor, Inc., 2-43 to 2-84 (1994)	

Examiner Signature		Dated Considered	
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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P. O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORM TO THIS ADDRESS. Send To Commissioner For Patents, P. O. Box 1450, Alexandria, VA 22313-1450. If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2

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Examiner Name	COLEMAN, ERIC				
Sheet	4	of	4	Attorney Docket Number	43876-155

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate) title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issued number(s), publisher, city and/or country where published.	T <sup>2</sup>
	AM	Asprey et al., "Performance Features of the PA7100 Microprocessor," IEEE Micro, 22-35 (June 1993)	
	AN	BSP and BSP Customer Attributes, Inclosure 5, Burroughs Corporation (Aug. 1, 1977)	
	AO	BSP Floating Point Arithmetic, Burroughs Corporation, (Dec. 1978)	
	AP	BSP Implementation of Fortran, Burroughs Corporation (Feb. 1978)	
	AQ	BSP, Burroughs Scientific Process, Burroughs Corporation, 1-29 (June 1977)	
	AR	Bursky, "Synchronous DRAMs Clock At 100 MHz," Electronic Design, Vol. 41, No. 4, 45-49 (Feb. 18, 1993)	
	AS	D. D. Gajski and L. P. Rubinfeld, "Design of Arithmetic Elements for Burroughs Scientific Processor," Proceedings of the 4th Symposium on Computer Arithmetic, Santa Monica, CA, 245-56 (1978)	
	AT	"System Architecture," ELXSI (2d Ed. Oct. 1983)	
	AU	"System Foundation Guide," ELXSI (1st Ed. Oct. 1987)	
	AV	L. Higbie, "Applications of Vector Processing," Computer Design, 139-45 (Apr. 1978)	
	AW	Ide et al., "A 320-MFLOPS CMOS Floating-Point Processing Unit for Superscalar Processors," IEEE Journal of Solid-State Circuits, Vol. 28, No.3, 352-61 (Mar. 1993)	
	AX	"IEEE Standard for Communicating Among Processors and Peripherals Using Shared Memory (Direct Memory Access—DMA)," IEEE (1994)	
	AY	D.A. Kuck & R. Stokes, "The Burroughs Scientific Processor (BSP)," IEEE Transactions on Computers, vol. C-31, No. 5, 363-76 (May 1982)	
	AZ	Lee, "Accelerating Multimedia with Enhanced Microprocessors," IEEE Micro, Vol. 15, No.2, 22-32 (Apr. 1995)	
	BA	Lion Extension Architecture (Oct. 12, 1991)	
	BB	"MC88110 Second Generation RISC Microprocessor User's Manual," Motorola (1991)	
	BC	Spaderna et al., "An Integrated Floating Point Vector Processor for DSP and Scientific Computing," IEEE International Conference on Computer Design: VLSI in Computers and Processors, 8-13 (Oct. 1989)	
	BD	Sprunt et al., "Priority-Driven, Preemptive I/O Controllers for Real-Time Systems," IEEE (1988)	
	BE	Thornton, "Design of a Computer-The Control Data 6600," Scott, Foresman and Company (1970)	
	BF	Watkins et al., "A Memory Controller with an Integrated Graphics Processor," IEEE, 324-36 (1993)	
	BG	Patent Family Tree of Patent 5,742,840	

Examiner Signature		Dated Considered	
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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P. O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORM TO THIS ADDRESS. Send To Commissioner For Patents, P. O. Box 1450, Alexandria, VA 22313-1450. If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2

# Patent Family Tree

## U.S. Patent No. 5,742,840

Last Modified  
6/20/2007

Patent No.: 5,742,840	Reexam No.: 90/007,583
Issue Date: 04/21/98	Status: Pending
Appln No.: 08/516,036	Response to First Office Action filed 02/22/07
Filing Date: 08/16/95	
Title:	General Purpose Multiple Precision Parallel Operation, Programmable Media Processor
Comments:	- Involved in MicroUnity v. Intel and Dell (Civil Action 2-04-CV-120 E.D. Tex. - settled) - MicroUnity v. Sony (Civil Action 2-05-CV-505 E.D. Tex. - pending) - MicroUnity v. AMD (Civil Action 2-06-CV-486 E.D. Tex. - pending)

DIV		DIV		DIV		CIP	
Patent No.: 5,794,061	Reexam No.: 90/007,563	Patent No.: 5,809,321	Reexam No.: 90/007,531	Patent No.: 5,794,060	Reexam No.: 90/008,593	Patent No.: 5,953,241	
Issue Date: 08/11/98	Status: Pending	Issue Date: 09/15/99	Status: Pending	Issue Date: 08/11/98	Status: Pending	Issue Date: 08/14/99	
Appln No.: 08/754,829	Response to First Office Action filed 02/27/07	Appln No.: 08/754,429	Response to First Office Action filed 07/05/06	Appln No.: 08/754,826	Response to First Office Action filed 02/22/07	Appln No.: 08/857,396	
Filing Date: 11/22/96		Filing Date: 11/22/96		Filing Date: 11/22/96		Filing Date: 05/16/97	
Title:	General Purpose Multiple Precision Parallel Operation, Programmable Media Processor	Title:	General Purpose Multiple Precision Parallel Operation, Programmable Media Processor	Title:	General Purpose Multiple Precision Parallel Operation, Programmable Media Processor	Title:	Multiplier Array Processing System with Enhanced Utilization at Lower Precision for Group Multiply and Sum Instruction
Comments:	- MicroUnity v. Intel and Dell (Civil Action 2-04-CV-120 E.D. Tex. - settled) - MicroUnity v. Sony (Civil Action 2-05-CV-505 E.D. Tex. - pending) - MicroUnity v. AMD (Civil Action 2-06-CV-486 E.D. Tex. - pending)	Comments:	- MicroUnity v. Intel and Dell (Civil Action 2-04-CV-120 E.D. Tex. - settled) - MicroUnity v. Sony (Civil Action 2-05-CV-505 E.D. Tex. - pending) - MicroUnity v. AMD (Civil Action 2-06-CV-486 E.D. Tex. - pending)	Comments:	- MicroUnity v. Intel and Dell (Civil Action 2-04-CV-120 E.D. Tex. - settled) - MicroUnity v. Sony (Civil Action 2-05-CV-505 E.D. Tex. - pending) - MicroUnity v. AMD (Civil Action 2-06-CV-486 E.D. Tex. - pending, but patent NO LONGER ASSERTED in this action)	Comments:	

Patent No.: 6,006,318	Reexam No.: 90/008,512
Issue Date: 12/21/99	Status: Pending
Appln No.: 09/169,963	Response to First Office Action filed 05/08/07
Filing Date: 10/13/98	
Title:	General Purpose Dynamic Partitioning Programmable Media Processor
Comments:	- MicroUnity v. AMD (Civil Action 2-06-CV-486 E.D. Tex. - pending)

Patent No.: 6,295,599	Reexam No.: 90/008,512
Issue Date: 09/25/01	Status: Pending
Appln No.: 09/382,402	Response to First Office Action filed 05/08/07
Filing Date: 08/24/99	
Title:	System With Wide Operand Architecture and Method (Zeus)
Comments:	Also claims priority to Provisional 60/097,635 filed 08/24/98.

CIP		CON	
Patent No.: 6,378,060	Reexam No.: 95/000,100	Patent No.: 6,643,765	Reexam No.: 95/000,089
Issue Date: 04/23/02	Status: Pending	Issue Date: 11/04/2003	Status: Pending
Appln No.: 09/502,856	Response to 28 Office Action filed 06/20/05	Appln No.: 09/534,745	Response to 28 Office Action filed 05/02/06
Filing Date: 02/11/00		Filing Date: 03/24/00	
Title:	System Method to Implement a Cross-Bar Switch of a Broadband Processor (XLU extensions)	Title:	System With Wide Operand Architecture and Method (Zeus)
Comments:	Also claims priority to Provisional 60/119,841 filed 02/12/99.	Comments:	- MicroUnity v. Intel and Dell (Civil Action 2-04-CV-120 E.D. Tex. - settled) - MicroUnity v. Sony (Civil Action 2-05-CV-505 E.D. Tex. - pending) - MicroUnity v. AMD (Civil Action 2-06-CV-486 E.D. Tex. - pending)

A

B

#610686599.xls

A

B

Pub. No.: 2003/01/0197 Pub. Date: 06/12/03 Appln No.: 10233/779 Filing Date: 09/04/02	Status: Pending. File lost then found 04/02/07. first Office Action not yet issued
Title: System and Method to Implement a Matrix Multiply Unit of a Broadband Processor	
Comments: Also claims priority to Provisional 60/317,427 filed 09/04/01.	

Pub. No.: 2004/0098548 Pub. Date: 05/20/04 Appln No.: 10816,303 Filing Date: 07/10/03	Status: Allowed. Notice of Allowance mailed 03/22/07
Title: Programmable Processor and Method with Wide Operands	
Comments: Also claims priority to Provisional 60/394,665 filed 07/10/02.	

Patent No.: 7,216,217 Issue Date: 05/08/07 Appln No.: 10646,787 Filing Date: 08/25/03	Status: Pending
Title: Programmable Processor with Group Floating-Point Instructions	
Comments: MicroUnity v. AMD (Civil Action 2:06-CV-486 E.D.Tex. - pending)	

Pub. No.: 2004/0049663 Pub. Date: 03/11/04 Appln No.: 10436,340 Filing Date: 05/13/03	Status: Pending. First Office Action in RCE case mailed 02/21/07
Title: System with Wide Operand Architecture and Method	
Comments:	

Pub. No.: 11/346,213 Pub. Date: 02/03/06 Appln No.: 11/030,006	Status: Pending
Title: Programmable Processor and Method with Wide Operands	
Comments:	

Pub. No.: 11/511,466 Pub. Date: 08/29/06 Appln No.: 11/030,006	Status: Pending
Title: Programmable Processor With Group Floating Point Instructions	
Comments:	

Pub. No.: 2004/013266 Pub. Date: 05/27/04 Appln No.: 10705,946 Filing Date: 11/13/03	Status: Allowed. Issue Fee paid 02/22/07. Resp. to 312 Amtd. Mailed 03/20/07
Title: Programmable Processor and System for Partitioned Group Shift	
Comments:	

Patent No.: 7,222,225 Issue Date: 05/22/07 Appln No.: 10716,561 Filing Date: 11/20/2003	Status: Pending
Title: Programmable Processor and System for Matched Aligned and Unaligned Storage Instructions	
Comments: MicroUnity v. AMD (Civil Action 2:06-CV-486 E.D.Tex. - pending)	

Pub. No.: 2004/0210745 Pub. Date: 10/21/04 Appln No.: 10757,939 Filing Date: 01/16/04	Status: Pending Resp. 2 <sup>nd</sup> OIA filed 06/8/07
Title: Multithreaded Programmable Processor and System with Partitioned Operations	
Comments:	

Pub. No.: 2004/0210746 Pub. Date: 10/21/04 Appln No.: 10757,936 Filing Date: 01/16/04	Status: Pending 2 <sup>nd</sup> OIA Action Mailed 02/21/07
Title: Programmable Processor and System for Store Multiplex Operation	
Comments:	

Patent No.: 7,213,131 Issue Date: 05/01/07 Appln No.: 10757,524 Filing Date: 01/15/04	Status: Pending
Title: Programmable Processor and Method for Group Element Selection	
Comments: MicroUnity v. AMD (Civil Action 2:06-CV-486 E.D.Tex. - pending)	

Pub. No.: 2004/0205096 Pub. Date: 10/14/04 Appln No.: 10757,836 Filing Date: 01/15/04	Status: Pending
Title: Programmable Processor and System for Partitioned Floating-Point Multiply-Add Operation	
Comments:	

Pub. No.: 2004/0098567 Pub. Date: 05/20/04 Appln No.: 10712,430 Filing Date: 11/14/03	Status: Pending, Rsp To 1 <sup>st</sup> OIA filed 05/09/07
Title: Method and Software for Partitioned Group Shift	
Comments:	

Pub. No.: 2004/0158689 Pub. Date: 08/12/04 Appln No.: 10716,568 Filing Date: 11/20/2003	Status: Pending. Response to 2 <sup>nd</sup> OIA, filed 05/09/07
Title: Method and Software for Matched Aligned and Unaligned Storage Instructions	
Comments:	

Pub. No.: 2004/0215942 Pub. Date: 10/28/04 Appln No.: 10757,915 Filing Date: 01/15/04	Status: Pending. Resp. 2 <sup>nd</sup> OIA filed 05/29/07
Title: Method and Software for Multithreaded Processor with Partitioned Operations	
Comments:	

Pub. No.: 2004/0205325 Pub. Date: 10/14/04 Appln No.: 10757,866 Filing Date: 01/16/04	Status: Pending, 2 <sup>nd</sup> OIA mailed 02/21/07
Title: Method and Software for Store Multiplex Operation	
Comments:	

Pub. No.: 2004/0153632 Pub. Date: 08/05/04 Appln No.: 10757,925 Filing Date: 01/16/04	Status: Pending, 2 <sup>nd</sup> OIA mailed 04/05/07
Title: Method and Software for Partitioned Group Element Selection Operation	
Comments:	

Pub. No.: 2004/0205324 Pub. Date: 10/14/04 Appln No.: 10757,851 Filing Date: 01/16/04	Status: Pending, 2 <sup>nd</sup> OIA mailed 02/21/07
Title: Method and Software for Partitioned Floating-Point Multiply-Add Operation	
Comments:	